	L#	Hits	Search Text	DBs
1	L15	451	spinlock\$3 spinloop\$3 (spin adj2 (lock\$3 loop\$3))	USPAT; US-PGPUB
2	L16	47300	(thread\$3 multithread\$3).ab,ti.	USPAT; US-PGPUB
3	L17	47	15 and 16	USPAT; US-PGPUB
4	L18	351295	(thread\$3 multithread\$3) not (sew\$3 knit\$4 tension)	USPAT; US-PGPUB
5	L20	37	17 and 18	USPAT; US-PGPUB
6	L21	24	(spinlock\$3 spinloop\$3 (spin adj2 (lock\$3 loop\$3))) and (((thread\$3 multithread\$3) not (sew\$3 knit\$4 tension)) and (thread near20 (halt\$3 stop\$4 suspend\$3 resum\$3)))	USPAT; US-PGPUB

•	Documen t ID	υ	Title	Current OR
1	US 2002002 9239 A1		Method and system for enhanced concurrency in a computing environment	709/104
2	US 2002001 6879 A1	☒	Resource locking and thread synchronization in a multiprocessor environment	710/200
3	US 2002000 4966 A1	Ø	Painting apparatus	15/257.06
4	US 2001005 6456 A1	☒	PRIORITY BASED SIMULTANEOUS MULTI-THREADING	709/103
5	US 2001005 2053 A1		Stream processing unit for a multi-streaming processor	711/138
6	US 2001003 7448 A1	☒	Input replicator for interrupts in a simultaneous and redundantly threaded processor	712/244
7	US 2001001 4905 A1	☒	Method and apparatus for managing a lock for an object	709/102
8	US 6378004 B1	☒	Method of communicating asynchronous elements from a mini-port driver	709/321
9	US 6330528 B1	Ø	Method of terminating temporarily unstoppable code executing in a multi-threaded simulated operating system	703/22
10	US 6314563 B1	☒	Expedited object locking and unlocking	717/108
11	US 6298370 B1	☒	Computer operating process allocating tasks between first and second processors at run time based upon current processor load	709/102
	115		Locking and unlocking mechanism for controlling concurrent access to objects	707/206
13	US 6179489 B1	☒	Devices, methods, systems and software products for coordination of computer main microprocessor and second microprocessor coupled thereto	709/102
14	US 6173442 B1	☒	Busy-wait-free synchronization	717/141
15	US 6167424 A	☒	Method and apparatus for concurrent thread synchronization	709/100
16	US 6105119 A	☒	Data transfer circuitry, DSP wrapper circuitry and improved processor devices, methods and systems	711/219
17	US 6047323 A	Ø	Creation and migration of distributed streams in clusters of networked computers	709/227
18	US 5937187 A	Ø	Method and apparatus for execution and preemption control of computer process entities	709/104
19	US 5909559 A	☒	Bus bridge device including data bus of first width for a first processor, memory controller, arbiter circuit and second processor having a different second data width	710/307
20	US 5872963 A	☒	Resumption of preempted non-privileged threads with no kernel intervention	712/233
21	US 5835964 A	☒	Virtual memory system with hardware TLB and unmapped software TLB updated from mapped task address maps using unmapped kernel address map	711/207
22	US 5819112 A	⊠.	Apparatus for controlling an I/O port by queuing requests and in response to a predefined condition, enabling the I/O port to receive the interrupt requests	710/36
23	US 5524247 A	☒	System for scheduling programming units to a resource based on status variables indicating a lock or lock-wait state thereof	710/200
24	US 5515538 A	☒	Apparatus and method for interrupt handling in a multi-threaded operating system kernel	710/260

	Documen t ID	U	Title	Current OR
1	US 2002001 6879 A1		Resource locking and thread synchronization in a multiprocessor environment	710/200
2	4966 A1	☒	Painting apparatus	15/257.06
3	6456 A1	☒	PRIORITY BASED SIMULTANEOUS MULTI-THREADING	709/103
4	•	☒	Input replicator for interrupts in a simultaneous and redundantly threaded processor	712/244
5	4827 A1		Active load address buffer	712/225
6	US 2001001 4905 A1	☒	Method and apparatus for managing a lock for an object	709/102
7	DI	☒	Method of communicating asynchronous elements from a mini-port driver	709/321
8	*	☒	Method and computer program product for reducing lock contention in a multiple instruction execution stream processing environment	707/8
9	US 6330528 B1	☒	Method of terminating temporarily unstoppable code executing in a multi-threaded simulated operating system	703/22
10	B1	☒	Expedited object locking and unlocking	717/108
11	US 6247025 B1	Ø	Locking and unlocking mechanism for controlling concurrent access to objects	707/206
12	US 6219690 B1	☒	Apparatus and method for achieving reduced overhead mutual exclusion and maintaining coherency in a multiprocessor system utilizing execution history and thread monitoring	709/102
13	B1	☒	Method and apparatus for user side scheduling in a multiprocessor operating system program that implements distributive scheduling of processes	709/107
14	B1	☒	Busy-wait-free synchronization	717/141
15	US 6167424 A	☒	Method and apparatus for concurrent thread synchronization	709/100
16	US 6119115 A	☒	Method and computer program product for reducing lock contention in a multiple instruction execution stream processing environment	707/8
17	US 6112222 A	☒	Method for resource lock/unlock capability in multithreaded computer environment	709/102
18	US 6105050 A		System for resource lock/unlock capability in multithreaded computer environment	709/102
19	US 6105049 A	Ø	Resource lock/unlock capability in multithreaded computer environment	709/102
20		☒	Creation and migration of distributed streams in clusters of networked computers	709/227
21		☒	Method of an apparatus for producing thick/thin and/or color effects yarn	57/287
22	US 5937187 A	☒	Method and apparatus for execution and preemption control of computer process entities	709/104
23	IIS :		Method for contention-free access and management of timers in multiprocessing environments	713/502
24	US 5872963 A	Ø	Resumption of preempted non-privileged threads with no kernel intervention	712/233

	Documen t ID	υ	Title	Current OR
25	US 5826081 A	Ø	Real time thread dispatcher for multiprocessor applications	709/103
26	US 5778733 A	Ø	Spinning nut linear mechanical lock	74/527
27	US 5727209 A	Ø	Apparatus and method for achieving reduced overhead mutual-exclusion and maintaining coherency in a multiprocessor system utilizing execution history and thread monitoring	709/102
28	US 5608893 A	Ø	Method for maintaining data coherency using thread activity summaries in a multicomputer system	711/141
29	US 5524247 A	\boxtimes	System for scheduling programming units to a resource based on status variables indicating a lock or lock-wait state thereof	710/200
30	US 5515538 A	☒	Apparatus and method for interrupt handling in a multi-threaded operating system kernel	710/260
31	US 5481706 A	\boxtimes	System and method for creating thread-safe shared libraries	710/200
32	US 5442758 A		Apparatus and method for achieving reduced overhead mutual exclusion and maintaining coherency in a multiprocessor system utilizing execution history and thread monitoring	707/8
33	US 5331837 A	☒	Vehicle door alignment device	72/458
34	US 5320413 A	\boxtimes	Vehicle seat assembly with linear actuator	297/362.12 ·
35	US 5179702 A	Ø	System and method for controlling a highly parallel multiprocessor using an anarchy based scheduler for parallel execution thread scheduling	709/102
36	US 3998164 A	\boxtimes	Self-destruct delay fuze	102/226
37	US 3700391 A	☒	AIR YARN TEXTURIZER	57/333